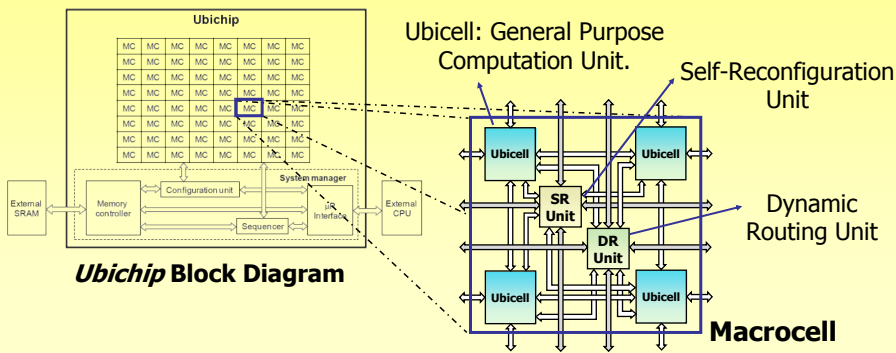


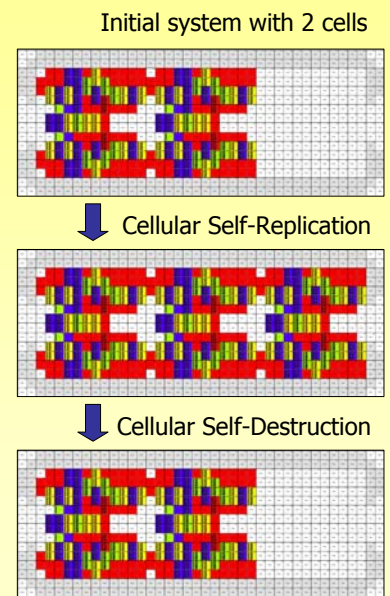
# The Ubichip

The *Ubichip* is a reconfigurable digital circuit endowed with bio-inspired mechanisms. The ubichip features a distributed self-reconfiguration and dynamic routing capabilities, for supporting self-replicating and self-connecting adaptive hardware systems. The reconfigurable array can also be configured as an SIMD multiprocessor architecture.

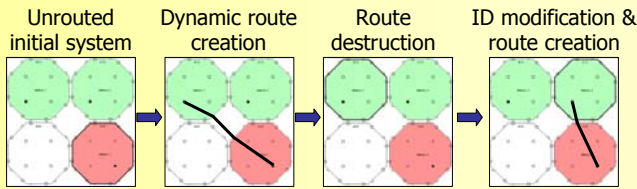
## Ubichip Architecture



## Self-Reconfiguration

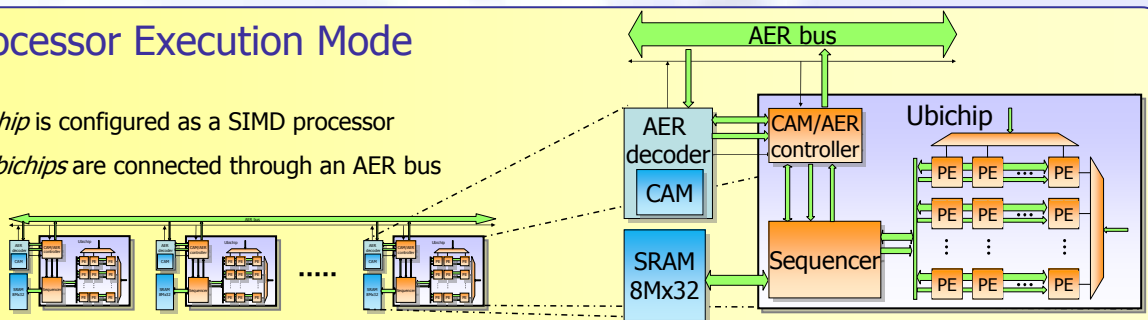


## Dynamic Routing



## Multiprocessor Execution Mode

- Each *Ubichip* is configured as a SIMD processor
- Several *Ubichips* are connected through an AER bus



## The UbiManager tool

- Design
- Simulation
- Debugging
- Monitoring
- Deployment support
- Multi-platform support

